## PALM INTRANET

# Inventor Information for 10/777508

| Inventor Name | City | State/Country |
|---|---|---|
| ALDEREGUIA, ALFREDO | CARY | NORTH CAROLINA |
| BEGUN, RALPH MURRAY | RALEIGH | NORTH CAROLINA |
| RICHTER, GRACE ANN | RALEIGH | NORTH CAROLINA |

| Appln Info | Contents | Petition Info | Atty/Agent Info | Continuity/Reexam | Foreign [ |

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## PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = ALDEREGUIA
First Name = ALFREDO

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 07590978 | 5522064 | 150 | 10/01/1990 | DATA PROCESSING APPARATUS FOR DYNAMICALLY SETTING TIMINGS IN A DYNAMIC MEMORY SYSTEM | ALDEREGUIA, ALFREDO |
| 07591306 | 5226134 | 150 | 10/01/1990 | A DATA PROCESSING SYSTEM INCLUDING A MEMORY CONTROLLER FOR DIRECT OR INTERLEAVE MEMORY ACCESSING | ALDEREGUIA, ALFREDO |
| 07815737 | Not Issued | 161 | 12/30/1991 | PERSONAL COMPUTER WITH INTERLEAVED MEMORY SYSTEM FOR STORING A DATA PACKET USING A STAGGERED CAS PROTOCOL | ALDEREGUIA, ALFREDO |
| 07816203 | 5255374 | 150 | 01/02/1992 | BUS INTERFACE LOGIC FOR COMPUTER SYSTEM HAVING DUAL BUS ARCHITECTURE | ALDEREGUIA, ALFREDO |
| 07816693 | Not Issued | 166 | 01/02/1992 | COMPUTER SYSTEM WITH VARIED DATA TRANSFER SPEEDS BETWEEN SYSTEM COMPONENTS AND MEMORY | ALDEREGUIA, ALFREDO |
| 08177078 | 5481552 | 250 | 12/30/1993 | METHOD AND STRUCTURE FOR PROVIDING ERROR CORRECTION CODE FOR 8-BYTE DATA WORDS ON SIMM CARDS | ALDEREGUIA, ALFREDO |
| 08293411 | 5761533 | 150 | 08/19/1994 | COMPUTER SYSTEM WITH VARIED DATA TRANSFER SPEEDS BETWEEN SYSTEM | ALDEREGUIA, ALFREDO |

| | | | | COMPONENTS AND MEMORY | |
|---|---|---|---|---|---|
| 10777508 | Not Issued | 30 | 02/12/2004 | Automated topology detection in a data processing system | ALDEREGUIA, ALFREDO |
| 11064752 | Not Issued | 30 | 02/24/2005 | Method and apparatus for testing, characterizing and monitoring a chip interface using a second data path | ALDEREGUIA, ALFREDO |
| 11100680 | Not Issued | 30 | 04/06/2005 | System and method for implementing a cable system | ALDEREGUIA, ALFREDO |
| 11101272 | 7294786 | 150 | 04/06/2005 | SYSTEM AND METHOD FOR MANAGING A CABLE IN A SERVER SYSTEM | ALDEREGUIA, ALFREDO |
| 11143414 | Not Issued | 71 | 06/02/2005 | Apparatus, system, and method for automatically detecting a cable configuration | ALDEREGUIA, ALFREDO |
| 11170951 | Not Issued | 30 | 06/30/2005 | Redundant 3-wire communication system and method | ALDEREGUIA, ALFREDO |
| 11277868 | Not Issued | 30 | 03/29/2006 | APPARATUS, SYSTEM, AND METHOD FOR ERROR ASSESSMENT OVER A COMMUNICATION LINK | ALDEREGUIA, ALFREDO |
| 11305932 | Not Issued | 25 | 12/19/2005 | Dynamic determination of signal quality in a digital system | ALDEREGUIA, ALFREDO |
| 11315837 | Not Issued | 30 | 12/22/2005 | Programmable throttling in blade/chassis power management | ALDEREGUIA, ALFREDO |
| 11419533 | Not Issued | 30 | 05/22/2006 | Communicating with Error Checking to a Device Capable of Operating According to an Address Prefix Serial Bus Protocol | ALDEREGUIA, ALFREDO |

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## Inventor Name Search Result

Your Search was:

Last Name = BEGUN
First Name = RALPH

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 11092188 | Not Issued | 30 | 03/29/2005 | System management architecture for multi-node computer system | BEGUN, RALPH M. |
| 11312019 | Not Issued | 30 | 12/20/2005 | Authentication of I²C bus transactions | BEGUN, RALPH M. |
| 11382080 | Not Issued | 30 | 05/08/2006 | SECURING LEASED RESOURCES ON A COMPUTER | BEGUN, RALPH M. |
| 07198890 | 5175826 | 150 | 05/26/1988 | DELAYED CACHE WRITE ENABLE CIRCUIT FOR A DUAL BUS MICROCOMPUTER SYSTEM WITH AN 80386 AND 82385 | BEGUN, RALPH M. |
| 07198893 | Not Issued | 168 | 05/26/1988 | METHOD AND APPARATUS FOR SELECTIVELY POSTING WRITE CYCLES USING THE 82385 CACHE CONTROLLER | BEGUN, RALPH M. |
| 07198894 | 5125084 | 150 | 05/26/1988 | CONTROL OF PIPELINED OPERATION IN A MICROCOMPUTER SYSTEM EMPLOYING DYNAMIC BUS SIZING WITH 80386 PROCESSOR AND 82385 CACHE CONTROLLER | BEGUN, RALPH M. |
| 07358807 | Not Issued | 166 | 05/31/1989 | MICROCOMPUTER SYSTEM EMPLOYING ADDRESS OFFSET MECHANISM TO INCREASE THE SUPPORTED CACHE MEMORY CAPACITY | BEGUN, RALPH M. |
| 07358810 | 5182809 | 150 | 05/31/1989 | DUAL BUS MICROCOMPUTER SYSTEM WITH PROGRAMMABLE CONTROL OF LOCK FUNCTION | BEGUN, RALPH M. |
| 07359794 | 5045998 | 250 | 06/01/1989 | METHOD AND APPARATUS FOR SELECTIVELY POSTING | BEGUN, RALPH M. |

| | | | | | |
|---|---|---|---|---|---|
| | | | | WRITE CYCLES USING THE 82385 CACHE CONTROLLER | |
| 07367653 | 5109506 | 150 | 06/19/1989 | MICROCOMPUTER SYSTEM INCLUDING A MICROPROCESSOR RESET CIRCUIT | BEGUN, RALPH M. |
| 07367828 | 5170481 | 150 | 06/19/1989 | MICROPROCESSOR HOLD AND LOCK CIRCUITRY | BEGUN, RALPH M. |
| 07368449 | 5146582 | 150 | 06/19/1989 | SYSTEM FOR USING A FIRST ADDRESS OF A BURST MODE DATA TRANSFER TO GENERATE EACH ADDRESS OF A PIPELINE MODE BEFORE CORRESPONDING BURST ADDRESSES ARE GENERATED | BEGUN, RALPH M. |
| 07696809 | 5327545 | 250 | 05/07/1991 | DATA PROCESSING APPARATUS FOR SELECTIVELY POSTIONG WRITE CYCLES USING THE 82385 CACHE CONTROLLER | BEGUN, RALPH M. |
| 07771528 | 5450559 | 250 | 10/07/1991 | MICROCOMPUTER SYSTEM EMPLOYING ADDRESS OFFSET MECHANISM TO INCREASE THE SUPPORTED CACHE MEMORY CAPACITY | BEGUN, RALPH M. |
| 07777844 | 5878256 | 150 | 10/16/1991 | METHOD AND APPARATUS FOR PROVIDING UPDATED FIRMWARE IN A DATA PROCESSING SYSTEM | BEGUN, RALPH M. |
| 07799486 | 5826075 | 150 | 11/27/1991 | AUTOMATED PROGRAMMABLE FIRMWARE STORE FOR A PERSONAL COMPUTER SYSTEM | BEGUN, RALPH M. |
| 07826733 | 5307482 | 250 | 01/28/1992 | COMPUTER, NON-MASKABLE INTERRUPT TRACE ROUTINE OVERRIDE | BEGUN, RALPH M. |
| 07861219 | Not Issued | 163 | 03/31/1992 | SELECTABLE PROGRAMMABLE FIRMWARE STORE FOR A PERSONAL COMPUTER SYSTEM | BEGUN, RALPH M. |
| 07905259 | 5459842 | 150 | 06/26/1992 | SYSTEM FOR COMBINING DATA FROM MULTIPLE CPU WRITE REQUESTS VIA BUFFERS AND USING READ-MODIFY-WRITE OPERATION TO WRITE THE COMBINED | BEGUN, RALPH M. |

| | | | | DATA TO THE MEMORY | |
|---|---|---|---|---|---|
| 08067361 | 5381541 | 250 | 05/26/1993 | COMPUTER SYSTEM HAVING PLANAR BOARD WITH SINGLE INTERRUPT CONTROLLER AND PROCESSOR CARD WITH PLURAL PROCESSORS AND INTERRUPT DIRECTOR | BEGUN, RALPH M. |
| 08148955 | 5375084 | 250 | 11/08/1993 | SELECTABLE INTERFACE BETWEEN MEMORY CONTROLLER AND MEMORY SIMMS | BEGUN, RALPH M. |
| 08152221 | Not Issued | 166 | 11/12/1993 | COMPUTER SYSTEM HAVING ROM CAPABILITIES ON DIFFERENT BUSES | BEGUN, RALPH M. |
| 08387383 | Not Issued | 166 | 02/13/1995 | COMPUTER SYSTEM AND METHOD OF OPERATION THEREOF WHEREIN A BIOS ROM CAN BE SELECTIVELY LOCATABLE ON DIFFERENT BUSES | BEGUN, RALPH M. |
| 08706934 | 5680556 | 150 | 09/03/1996 | COMPUTER SYSTEM AND METHOD OF OPERATION THEREOF WHEREIN A BIOS ROM CAN BE SELECTIVELY LOCATABLE ON DIFFERENT BUSES | BEGUN, RALPH M. |
| 09953761 | Not Issued | 161 | 09/17/2001 | System and method for performing power management on a distributed system | BEGUN, RALPH MURRAY |
| 10777508 | Not Issued | 30 | 02/12/2004 | Automated topology detection in a data processing system | BEGUN, RALPH MURRAY |
| 08837756 | 5802393 | 250 | 04/22/1997 | COMPUTER SYSTEM FOR DETECTING AND ACCESSING BIOS ROM ON LOCAL BUS PERIPHERAL BUS OR EXPANSION BUS | BEGUN, RALPH MURRAY |
| 09053986 | 5884094 | 250 | 04/02/1998 | COMPUTER SYSTEM FOR DETECTING AND ACCESSING BIOS ROM ON THE LOCAL OR PERIPHERAL BUS | BEGUN, RALPH MURRAY |

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## Inventor Name Search Result

Your Search was:

Last Name = RICHTER
First Name = GRACE

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 11100680 | Not Issued | 30 | 04/06/2005 | System and method for implementing a cable system | RICHTER, GRACE A. |
| 11101272 | 7294786 | 150 | 04/06/2005 | SYSTEM AND METHOD FOR MANAGING A CABLE IN A SERVER SYSTEM | RICHTER, GRACE A. |
| 11419533 | Not Issued | 30 | 05/22/2006 | Communicating with Error Checking to a Device Capable of Operating According to an Address Prefix Serial Bus Protocol | RICHTER, GRACE A. |
| 10777508 | Not Issued | 30 | 02/12/2004 | Automated topology detection in a data processing system | RICHTER, GRACE ANN |
| 11064752 | Not Issued | 30 | 02/24/2005 | Method and apparatus for testing, characterizing and monitoring a chip interface using a second data path | RICHTER, GRACE ANN |
| 11143414 | Not Issued | 71 | 06/02/2005 | Apparatus, system, and method for automatically detecting a cable configuration | RICHTER, GRACE ANN |
| 11170951 | Not Issued | 30 | 06/30/2005 | Redundant 3-wire communication system and method | RICHTER, GRACE ANN |
| 11277868 | Not Issued | 30 | 03/29/2006 | APPARATUS, SYSTEM, AND METHOD FOR ERROR ASSESSMENT OVER A COMMUNICATION LINK | RICHTER, GRACE ANN |

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